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Substitute for form 1449/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)		<b>Complete if Known</b>	
		Application Number	NOT KNOWN YET
		Filing Date	09/15/2003
		First Named Inventor	Chia-Ta Hsieh et al.
		Art Unit	NOT KNOWN YET
		Examiner Name	NOT KNOWN YET
Sheet 1 of 2	Attorney Docket Number	N1085-00125	

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
HT	A	US- 5,411,905	05-02-1995	Acovic et al.	
	B	US- 6,054,350	04-25-2000	Hsieh et al.	
	C	US- 6,074,914	06-13-2000	Ogura	
	D	US- 6,157,058	12-05-2000	Ogura	
	E	US- 6,248,633	06-19-2001	Ogura et al.	
	F	US- 6,326,661	12-04-2001	Dormans et al.	
	G	US- 2002/0045319 A1	04-18-2002	Ogura et al.	
	H	US- 2002/0109181 A1	08-15-2002	Hsieh et al.	
HT	I	US- 6,477,085	11-05-2002	Kuo	
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	†
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				
HT	J	JPO 2002170891 A	06-14-2002	Ogura, Seiki		
HT	K	WIPO PCT/JP01/10156	11/21/2001	Ogura, Seiki		

Examiner Signature	Huan Huan	Date Considered	9/12/04
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Sheet 2	of 2	Attorney Docket Number	N1085-00125

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
HT	L	VANDANA VERMA and ANDREW SWANECK, "Proposed On-Chip Test Structure To Quantify Trap Densities Within Flash Memories", Proceedings of the 1996 IEEE International Workshop On Memory Technology, Design and Testing (MTDT '96), pp. 22 - 26.	

Examiner Signature	Huan Huan	Date Considered	4/12/04
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